The CODAI workshop coalesces the emerging energy in the AI compiler communities and AI accelerator communities focusing on Edge AI in both academic and industrial research. Original technical research submissions as well as submissions from industry and applications on, but not limited to, the following topics are invited:

- Optimization techniques of neural networks on embedded edge systems: compression, pruning, quantization techniques, hardware/compiler-aware neural architecture search, etc.
- Performance estimation of neural networks on embedded systems: virtual prototyping, surrogate modeling, profiling etc.
- Compilers for Edge AI: partitioning, mapping, retargeting, intermediate representations, for heterogeneous systems and architectures, μC, DSP etc.
- Code-generation and compiler-backends for AI accelerators - especially for RISC-V and beyond-von-Neumann AI accelerators is appreciated
- Applications: processing of embedded vision, sensor signal processing, novel brain-inspired algorithms for Edge AI etc.

**Submission Details**

Submissions can be made on the CODAI website. Manuscripts with up to four (4) pages excluding references will be reviewed. The reviewing process is double-blinded. Therefore, please exclude all author’s information in the PDF. Full papers will be reviewed, preliminary submissions may not be considered. **Template:** please use the SigConf ACM style template in double columns format for submissions.

**Important Dates**

- Paper submission deadline: August 18, 2022 at 23:59 AOE
- Acceptance notification: September 8, 2022 at 23:59 AOE
- Camera-ready deadline: September 30, 2022 at 23:59 AOE
- Workshop date and time: October 13, 2022 at 16:00 CET until 20:00 CET