Call for Papers





September 17th - 22th 2023 | Hybrid Workshop, co-hosted at Embedded Systems Week

The **CODAI** workshop brings together the emerging energy in the AI compiler communities and AI accelerator communities focusing on **Edge AI** in both **academic and industrial research**. Original technical research submissions as well as submissions from industry and applications on, but not limited to, the following topics are invited:

- Compilers for Edge Al: partitioning, mapping, retargeting, intermediate representations, for heterogeneous systems and architectures, μC, DSP etc.
- Optimization techniques of neural networks on embedded edge systems: compression, pruning,
 quantization techniques, hardware/compiler-aware neural architecture search, etc.
- Performance estimation of neural networks on embedded systems: virtual prototyping,
 surrogate modeling, profiling etc.
- Code-generation and hardware-backends for Embedded AI accelerators especially for RISC-V and beyond-von-Neumann AI accelerators is appreciated
- Applications: processing of embedded vision, sensor signal processing, novel brain-inspired algorithms for Edge AI etc.

Submission Details

Submissions can be made on the <u>CODAI website</u>. Manuscripts with up to four (4) pages excluding references will be reviewed. The reviewing process is double-blinded. Therefore, please exclude all author's information in the PDF. Full papers will be reviewed, preliminary submissions may not be considered. Please use the submission <u>template</u> linked on the <u>CODAI website</u>.

Important Dates

- Abstract submission deadline: July 6, 2023 at 23:59 AOE
- Paper submission deadline: July 13, 2023 at 23:59 AOE
- Acceptance notification: August 10, 2023 at 23:59 AOE
- Camera-ready deadline: August 24, 2023 at 23:59 AOE